

***Day One, Tuesday, 29 September***

<b>Topic</b>	<b>Presenter</b>	<b>Time</b>
Arrivals		1:00
Welcome	Jay Boisseau	1:00 - 1:20
Kick-off: Dell in HPC - Dedicated to Discovery	Frank Muehleman	1:20 - 1:40
Site Update: TACC	Dr. Tommy Minyard	1:40 - 2:40
New Site Introduction: Harvard	Suvendra Dutta	2:40 - 3:30
Travel to UT Campus - ACES VizLab		3:30
Viz Lab Tour & Reception	Host: Melyssa Fratkin	4:00 -6:00
Session Ends: <i>YoYo Dinner</i>		

***Day Two, Wednesday, 30 September***

<b>Topic</b>	<b>Presenter</b>	<b>Time</b>
Arrivals & Breakfast		8:00 - 8:30
Partner Roadmap: Intel		8:30-9:30
Emerging Technologies: Terascale - Extending Lustre File System Reach & Usage	John Thuotte	9:30-10:30
Break		10:30-10:45
Partner Roadmap: AMD	Jeff Underhill	10:45-11:45
Site Update: NCSA	Mike Pflugmacher	11:45-12:15
Lunch		12:15-1:00
Usage Models: Wrangling Science for Safety	Gordon Wells	1:00-1:30
Site Update: MHPCC	Mike McCraney	1:30-2:00
Emerging Technologies: GPGPU	Ragu Shankar/Shawn Dube	2:00-3:00
Break		3:00-3:15
Precision Roadmap	Greg Weir	3:15-4:15
DCS Roadmap	<Doug Taylor>	4:15-5:15
Storage Roadmap	James Honey/Dan McConnell	5:15-6:15
Evening Reception/Dinner	Roaring Fork	6:30

***Day Three, Thursday, 01 October***

<b>Topic</b>	<b>Presenter</b>	<b>Time</b>
Arrivals & Breakfast		8:00 - 8:30
Engineering PoC	Jacob Liberman	8:30-9:30
PoC Characterizations: HPC Advisory Council	Gilad Shainer	9:30-10:30
Break		10:30-10:45
HPC Roadmap	Peter Lillian	10:45-11:30
VoC: Climbing from Peta to Exa Scale	Peter Lillian, Laura Micks	11:30-12:30
Consortium Business	Elections, Evolutions, SC09 Meeting	12:30-1:00
Session Ends		